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Confirmation of spontaneous doping of GaN nanowires grown on vicinal SiC/Si substrate by electron beam induced current mapping

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Abstract

This study is devoted to the confirmation of spontaneous doping of GaN nanowires grown on vicinal SiC/Si hybrid substrates by electron beam induced current mapping.

GaN nanowires (NWs) were grown on singular and vicinal SiC/Si substrates by molecular beam epitaxy with nitrogen plasma activation. The morphological properties of the NWs were studied by scanning electron microscopy. The electrophysical properties of the obtained nanostructures were studied by electron beam induced current mapping.

By electron beam induced current mapping, we confirmed the spontaneous doping of the GaN NWs grown on vicinal SiC/Si wafers. It was also shown that the GaN NWs grown on singular SiC/Si substrates did not exhibit an induced current signal, indicating that they were not doped.

Keywords: Semiconductors, GaN, Nanowires, Molecular beam epitaxy, Spontaneous doping, Silicon, Silicon carbide, Electron beam induced current method

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1. Introduction

Nowadays, wide-gap semiconductor nanostructures based on nitride compounds are of great interest to researchers due to their unique optical, electrophysical, transport, and other properties [1-3]. Nitride nanostructures are already used in a number of applications, such as LEDs, solar cells, transistors, single photon sources, and others [4–7]. Modern methods for the formation of semiconductor nanostructures make it possible to grow two-dimensional, one-dimensional, and zero-dimensional structures based on nitride compounds, as well as their combinations [8–11]. One of the most common methods for growing such nanostructures is molecular beam epitaxy (MBE). The advantages of this method include the ultrahigh vacuum level in the growth chamber, low growth rate, precise control over the growth processes, and, as a consequence, high crystallographic and optical quality of the resulting nanostructures [12–14]. It is important to note that nonplanar nitride nanostructures, such as nanowires (NWs), in some cases make it possible to increase the efficiency of devices or design a new generation of devices [15, 16]. Moreover, producing nanostructures in the form of nanowires allows us to solve the issue of integrating nitride compounds with mismatched substrates [17, 18]. Nevertheless, in most cases, to develop applications based on nitride nanowires, it is necessary to obtain nanostructure sites of P- and/or N-type conductivity to enable contact with the nanowires or the formation of the P-N junctions. To obtain efficient devices, it is necessary to consider all doping mechanisms of semiconductor nanostructures. As we demonstrated earlier, the substrate on which the nanostructures grow can also influence the type and level of doping of the nanowires [19]. In this study, we proved the fundamental possibility of growing GaN nanowires on singular and vicinal Si hybrid substrates with a thin SiC layer on the surface. Due to the optical studies, we observed the effect of spontaneous doping of the GaN nanowires grown on vicinal substrates with Si atoms. We also described the mechanism of doping of GaN nanowires with Si atoms from the SiC layer. It should be noted that the doping level of nanowires turned out to be higher than the solubility level of Si in GaN, which provides

prospects for new applications based on this material.

The aim of the study was to confirm the spontaneous doping of the GaN nanowires grown on vicinal SiC/Si hybrid substrates by electron beam induced current mapping (induced current method).

2. Experimental

The GaN nanowires were grown using a Riber Compact 12 MBE unit equipped with a Ga effusion source and a plasma nitrogen source. Singular and vicinal Si(111) wafers with a thin SiC buffer layer were used as substrates. The technology for growing GaN nanowires on SiC/Si substrates is described in detail in [19]. All samples were grown under identical conditions. During the first stage, the substrate was placed in the growth chamber and heated to 950 °C in order to thermally clean the surface. After holding the substrate at a high temperature for 20 min, the substrate temperature was lowered to the growth temperature of 870 °C. During the next stage, nitrogen plasma was initiated at the source power of 520 W, with the N⁺ flux rate of 1.5 sccm. Then, the Ga source was opened to grow GaN nanowires on the substrate surface. The total GaN nanowire growth time was 16 hours. The Ga flux from the source had a pressure of $1.6 \cdot 10^{-7}$ according to preliminary calibrations using the Bayard–Alpert gauge.

Changes in the surface morphology were recorded *in situ* by reflected high energy electron diffraction (RHEED), which indicated the wurtzite crystallographic phase of the growing nanostructures. The morphological properties of the grown nanostructures and the induced current maps were analyzed at room temperature using a Hitachi SU8000 scanning electron microscope (SEM) equipped with a Gatan DigiScan system for electron beam induced current mapping. Micro-manipulators (probes) connected to a SR570 low-noise current preamplifier were used to contact the grown nanostructures. The methodology of the induced current measurements is described in detail in [20].

3. Results and discussion

The GaN nanowires were formed in the (111) direction on the singular SiC/Si substrate and on

the flat surface of the vicinal SiC/Si substrate. However, the nanowires grown on the slopes of the vicinal substrate also had another direction. The average height of the GaN NWs was 1.5 μm , and their average diameter was 300 nm.

The doping of the GaN NWs grown on vicinal SiC/Si substrates was confirmed by electron beam induced current mapping. In the induced current experiment, a tungsten probe contacted the top of the GaN nanowire, which generated a built-in electric field near the probe/nanowire contact due to the formation of a Schottky barrier at the tungsten/GaN interface. This built-in electric field effectively separated the electron-hole pairs generated by the electron beam. So, the induced current signal could be registered by an external measurement circuit connected to the probes. Moreover, the doping level of GaN nanowires could be estimated by the size of the space charge region (SCR), which is plotted as a plateau on the signal profile on the induced current maps [21]. The resistivity of undoped nanowires was high, the field magnitude of the Schottky barrier was low, and, consequently, the size of the SCR was large. So, electron-hole pairs could not be effectively separated by the built-in electric field. Together with the high resistivity of the NWs, it resulted in a very small measured value of the induced current signal.

A typical SEM image with a research probe and a characteristic induced current map for the GaN NWs grown on a vicinal SiC/Si substrate are shown in Figs. 1a and 1b, respectively. In Fig. 1a, it can be seen that the probe touches the apex of the GaN NWs. As can be seen from Fig. 1b, a GaN nanowire grown on vicinal SiC/Si substrate has a clearly distinguishable region of the induced current signal near the probe/nanowire interface, indicating an extremely high degree of NW doping.

The opposite pattern was observed for the GaN NWs grown on singular SiC/Si substrates. Figures 2a and 2b show a typical SEM image with a research probe and a characteristic map of the induced current for such a nanowire. As can be seen from the figures, a nanowire grown on singular GaN substrate does not manifest the induced current signal, which proves that it was not doped.

4. Conclusions

Thus, we confirmed the spontaneous doping of GaN nanowires grown on vicinal SiC/Si substrates by electron beam induced current mapping. It was also shown that the GaN NWs grown on singular SiC/Si substrates did not exhibit an induced current signal, indicating that they were not doped.

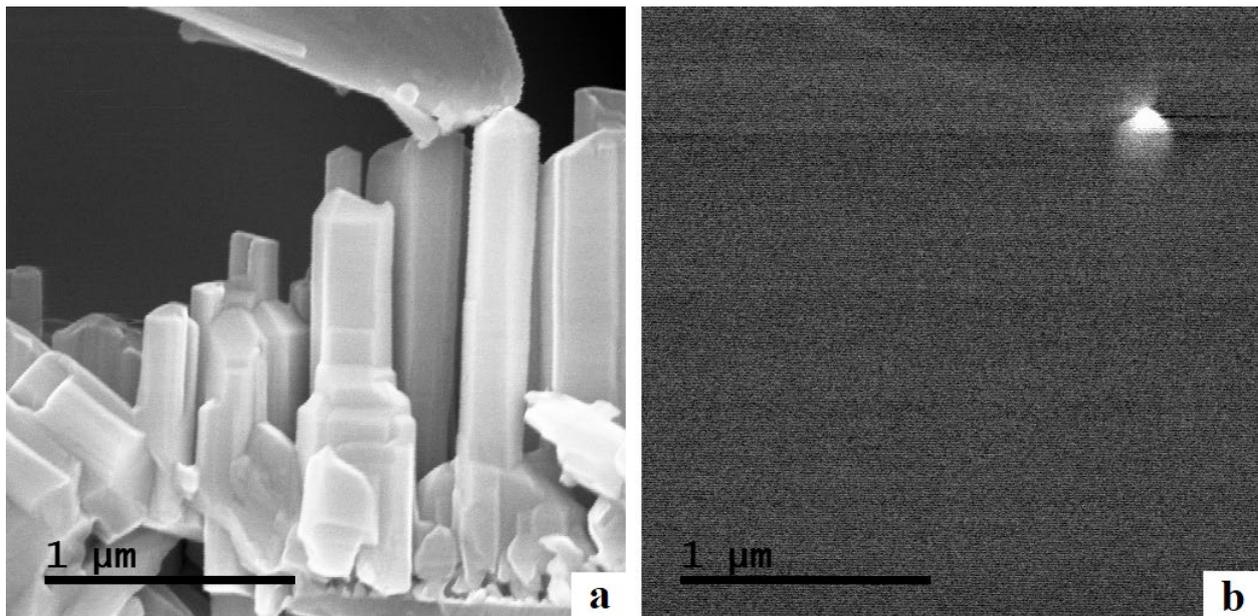


Fig. 1. Typical SEM image with a research probe (a) and a characteristic map of the induced current for a GaN NW grown on vicinal SiC/Si substrate (b)

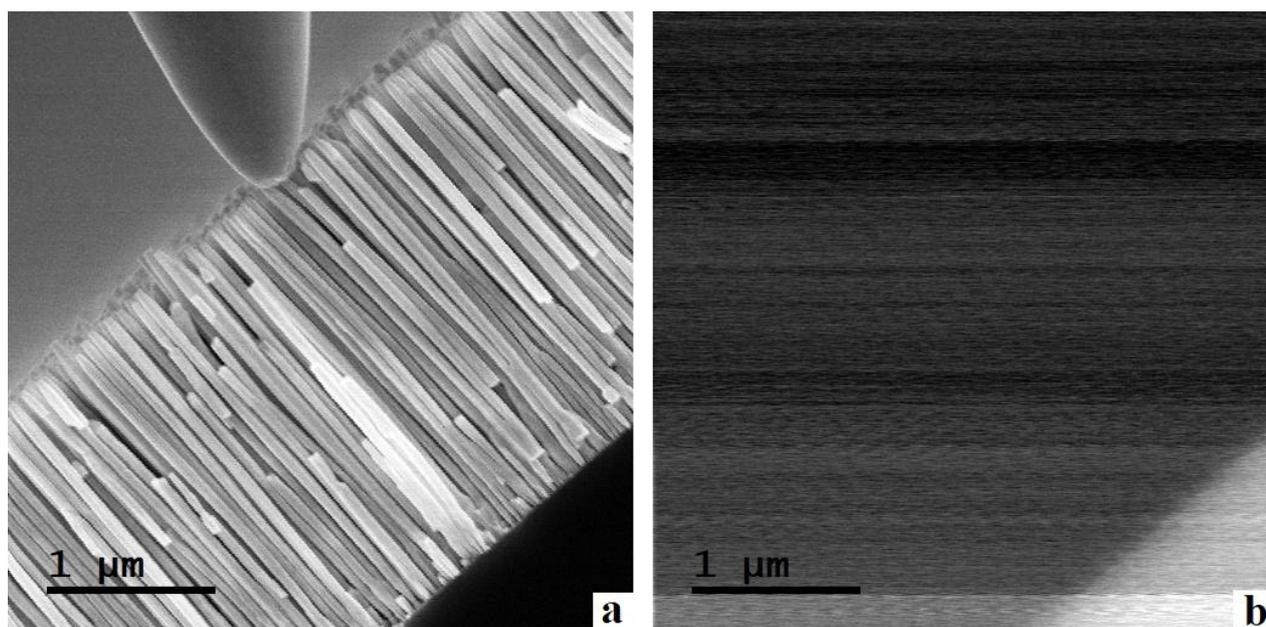


Fig. 2. Typical SEM image with a research probe (a) and a characteristic map of the induced current for a GaN NW grown on singular SiC/Si substrate (b)

Author contributions

R. R. Reznik – growing the samples, planning the experiment, text writing, and final conclusions. V. O. Gridchin – growing the samples. K. P. Kotlyar – SEM measurements of the samples. V. V. Neplokh – analysis of the results. A. V. Osipov – production of the substrates for growing the samples. S. A. Kukushkin – production of the substrates for growing the samples. O. Saket – study of the electrical properties of the samples. M. Tchernycheva – study of the electrophysical properties of the samples and analysis of the results. G. E. Cirlin – planning the experiments, analysis of the results.

Conflict of interests

The authors declare that they have no known competing financial interests or personal relationships that could have influenced the work reported in this paper.

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